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Application No.: 09/853,005

Docket No.: JCLA5312

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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In re the Patent Application of :

Chau-Chad Tsai,
Chi-Che Tsai and
Chen-Ping Yang

Examiner : BAKER, PAUL A

Serial No. : 09/853,005

Art Unit : 2188

Filed : May 09, 2001

Docket No. : JCLA5312

For : PERIPHERAL DEVICE
INTERFACE CHIP CACHE AND
DATA SYNCHRONIZATION
METHOD**AMENDMENT AND RESPONSE TO OFFICE ACTION**Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to a Third Office Action dated Sep. 17, 2003, Applicants respectfully request
to consider the following remarks.

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CERTIFICATE OF TRANSMISSION

December 23, 2003

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BY FACSIMILE ONLY

Fax No. :	703-872-9306
Attention :	EXAMINER : BAKER, PAUL A. Commissioner for Patents
Group Unit :	2187
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith is an Amendment with reference in 12 pages

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on **December 23, 2003** at the above indicated fax number.

Sign by: 
Michelle Chang

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